

PATENT ABSTRACTS OF JAPAN

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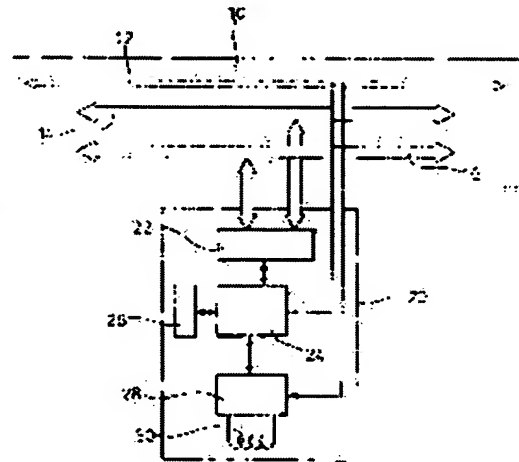
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(54) SOLENOID VALVE DRIVE CONTROL SYSTEM

(57)Abstract:

PURPOSE: To devise effective application of the mounting space of solenoid valves by making each solenoid receive address signals and on-off control signals related to a specific solenoid valve unit through common bus bars.

CONSTITUTION: Bus bars 10 connected to a plurality of solenoid valves comprises a power supply bus 12, an address bus 14, a data bus 16. A solenoid valve unit 20 has an input/output circuit 22, a CPU 24, an internal memory 26, a solenoid driver 28, and a solenoid 30 in it. Address signals related to a specific solenoid valve unit are fed to the address bus 14 in the bus bars 10, and at the simultaneous timing with the signals on-off signals of the solenoid forming the above mentioned specific valve are fed to the data bus 16. In this manner, the number of wirings for each solenoid valve is sharply reduced.



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